

**Amendments to the Specification:**

Please amend the specification as follows:

[0045] As mentioned in connection with Tables 1 and 2 above, FIG. 5 shows one way of reducing the memory required to store the 3.times.N matrices--in this case, a six primary system. It was also mentioned above that there would be a way to take advantage of the identical rows in the hardware. FIG. 6 shows how this can be done with a single 3.times.3 multiplier and 6 multiplexors for the same 6-primary system. This is just one example, the same savings can be done with any number of primaries. As the number of primaries increases, the number of inexpensive multiplexors increases, but the number of expensive multipliers remains constant at a single 3.times.3 multiplier.